Amendments to the Claims

Please amend Claims 1, 6 and 12. The Claim Listing below will replace all prior versions of the claims in the application:

Claim Listing

1. (Currently Amended) A portable communications device having a reflective display comprising:

a device housing having a wireless receiver;

an active matrix display having an active matrix circuit and an array of pixel electrodes, the active matrix circuit capable of storing charge between vertical synchronization signals;

a light emitting diode light source optically coupled to illuminate the matrix display for illuminating the image;

a display control circuit positioned in the housing and connected to the wireless receiver, the matrix display, and the light source such that image data received by the receiver is input to the display control circuit, which generates a display signal including a vertical synchronization signal to drive the matrix display to render the image;

an optical coupler that couples light from the light source onto the matrix display and the reflected light toward a viewer; and

a power management circuit that controls the power consumption of the display control circuit, the power management circuit lowering the power consumption of the display circuit between vertical synchronization signals, the power management circuit arranged for receiving control signals for lowering the power consumption, the control signals resulting from signals from the display control circuit that are initiated by the display control circuit, the power management circuit and the display control circuit being connected together and arranged in a configuration that lowers the power consumption in a self regulating manner <u>where the power consumption is lowered after a frame of data is written on the matrix display and raised when a new frame of data is written</u>.

2. (Previously Presented) The device of claim 1 wherein the pixel electrodes are reflective pixel electrodes and further comprising a transistor circuit formed with single crystal silicon associated with each pixel electrode.

3. (Previously Presented) The device of claim 2 further comprising a color sequential display circuit coupled to the matrix display and the control circuit.

4. (Previously Presented) The device of claim 3 further comprising a switching circuit connected to a counterelectrode panel of the matrix display for switching the applied voltage to the counterelectrode panel.

5. (Previously Presented) The device of claim 3 wherein the optical coupler includes a dichroic prism.

6. (Currently Amended) A portable communications device having a reflective color sequential display comprising:

a device housing having a wireless receiver;

an active matrix display having an active matrix circuit and an array of pixel electrodes, the active matrix circuit capable of storing charge between vertical synchronization signals;

a plurality of light emitting diodes that sequentially illuminate the display;

a color sequential display control circuit positioned in the housing and connected to the wireless receiver, the matrix display, and the light emitting diode such that image data that is received by the receiver is input to the display control circuit which generates a display signal including a vertical synchronization signal to drive the matrix display to render an image, and a timing signal to drive the light emitting diodes to illuminate the matrix display;

a dichroic prism for directing the light from the light emitting diodes to the active matrix display and coupling reflected light toward a viewer;

a battery for powering the matrix display, display control circuitry and the light emitting diodes; and

a power management circuit that controls the power consumption of the display control circuit, the power management circuit lowering the power consumption of the display circuit between vertical synchronization signals, the power management circuit arranged for receiving control signals for lowering the power consumption, the control signals resulting from signals from the display control circuit that are initiated by the display control circuit, the power management circuit and the display control circuit being connected together and arranged in a configuration that lowers the power consumption in a self regulating manner where the power consumption is lowered after a frame of data is written on the matrix display and raised when a new frame of data is written.

7. (Previously Presented) The device of claim 6 further comprising a diffuser positioned between the light emitting diodes and the dichroic prism.

8. (Previously Presented) The device of claim 7 further comprising at least one dichroic mirror for directing the light from one light emitting diode and allowing light from another light emitting diode to pass through.

9. (Withdrawn) The device of claim 6 wherein the device comprises a wireless pager.

10. (Original) The device of claim 6 wherein the device comprises a telephone.

11. (Withdrawn) The device of claim 6 wherein the device comprises a docking station for a wireless telephone.

12. (Currently Amended) A portable communications device having a reflective display comprising:

a device housing having a wireless receiver;

an active matrix liquid crystal display having an array of reflective pixel electrodes, and an active matrix circuit including a transistor circuit formed with single crystal silicon associated with each pixel electrode, the active matrix circuit storing charge between vertical synchronization signals,

a lens that focuses an image on the display for viewing by a user;

a plurality of light emitting diodes for illuminating the image;

a display control circuit positioned in the housing and connected to the wireless receiver, the matrix display, and the light emitting diodes such that image data that is received by the receiver is input to the display control circuit, which generates a display signal including a vertical synchronization signal to drive the matrix display to render the image;

a dichroic prism for directing the light from the light emitting diodes to the matrix display and coupling reflected light to the lens; and

a power management circuit that controls the power consumption of the display control circuit, the power management circuit lowering the power consumption of the display circuit between vertical synchronization signals, the power management circuit arranged for receiving control signals for lowering the power consumption, the control signals resulting from signals from the display control circuit that are initiated by the display control circuit, the power management circuit and the display control circuit being connected together and arranged in a configuration that lowers the power consumption in a self regulating manner where the power consumption is lowered after a frame of data is written on the matrix display and raised when a new frame of data is written.

13. (Previously Presented) The device of claim 12 wherein the display control circuit is a color sequential display circuit for sequentially illuminating the matrix display with the light emitting diodes.

14. (Previously Presented) The device of claim 12 wherein the matrix display has an array of at least 640 by 480 pixel electrodes.

15. (Previously Presented) The device of claim 12 further comprising a diffuser positioned between the light emitting diodes and the dichroic prism.

16. (Previously Presented) The device of claim 12 further comprising a pair of dichroic mirrors, each mirror directing the light from one light emitting diode and allowing light from at least another light emitting diode to pass through.

17. (Withdrawn) The device of claim 12 wherein the device comprises a camera.

18. (Original) The device of claim 12 wherein the device comprises a telephone.

19. (Withdrawn) The device of claim 12 wherein the device comprises a docking station for a telephone.

20. (Withdrawn) The device of claim 12 wherein the device comprises a pager.

21. (Canceled).

22. (Previously Presented) The device of claim 12 further comprising a pair of dichroic mirrors, each mirror for directing the light from one light emitting diode and allowing light from at least another light emitting diode to pass through.

23. (Previously Presented) The device of claim 22 wherein the display control circuit is a color sequential display circuit for sequentially illuminating the matrix display with the light emitting diodes.

24. (Previously Presented) The device of claim 23 further comprising a switching circuit connected to a counterelectrode panel of the matrix display for switching the applied voltage to the counterelectrode panel.

25. (Previously Presented) The device of claim 12 wherein the light emitting diodes comprise three light emitting diodes of three distinct colors.

26. (Previously Presented) The device of claim 25 further comprising at least one dichroic mirror for directing light from one light emitting diode and allowing light from another light emitting diode to pass through.

27. (Previously Presented) The device of claim 26 wherein the three light emitting diodes are flashed concurrently to emit white light.

28-36. (Canceled).

37. (Previously Presented) The device of Claim 1 wherein the power management circuit lowers the power consumption of the display circuit without comparing sequential image data.

38. (Previously Presented) The device of Claim 6 wherein the power management circuit lowers the power consumption of the display circuit without comparing sequential image data.

39. (Previously Presented) The device of Claim 12 wherein the power management circuit lowers the power consumption of the display circuit without comparing sequential image data